WEST Search History

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DATE: Monday, January 31, 2005

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		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L16	l13 not l14	13
☐	L15	L12 and render$3	18
☐	L14	L12 same render$3	5
☐	L13	L12 and render$3	18
☐	L12	cop$4 adj5 (graphic$3 or data or image) adj7 memory adj6 frame buffer	25
☐	L10	L9 and render$3	21
☐	L9	L1 same transfer$4 near4 data	28
☐	L8	L7 and render$3	2
☐	L7	L1 same cop$4 near4 data	2
☐	L6	L1 and cop$4 near4 data	44
☐	L5	L4 and $5processor	1
☐	L3	L1 and cop$4 near4 graphic$3 near5 memory	7
☐	L2	L1 same cop$4 near4 graphic$3 near5 memory	2
☐	L1	(ram or memory) same graphic$3 adj3 (engine or card or accelerator or module or device or block or unit or component or element or logic or $5processor) same (vram or (video near3 memory)) same frame buffer same (cpu or $5processor or proces$4)	238

END OF SEARCH HISTORY